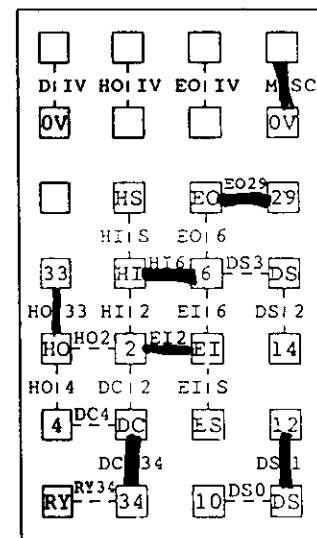


11. CUSTOMER SELECTABLE STRAPS

11.1 Function Summary of Straps

The FDD is equipped with the following selectable straps on the main PCBA. Insertion of a short bar onto the post pin is defined as the on-state of the strap. Refer to Table 1 in item 1 as to the strap setting at delivery.

Strap	Function
DS0	DRIVE SELECT 0 on pin 10
DS1	DRIVE SELECT 1 on pin 12
DS2	DRIVE SELECT 2 on pin 14
* DS3	DRIVE SELECT 3 on pin 6
* RY34	READY output on pin 34
* DC2	DISK CHANGE output on pin 2
* DC4	DISK CHANGE output on pin 4
* DC34	DISK CHANGE output on pin 34
* HIS	Automatic HD set by HD sensor
* HI2	HD IN input on pin 2
* HI6	HD IN input on pin 6
* EIS	Automatic ED set by ED sensor
* EI2	ED IN input on pin 2
* EI6	ED IN input on pin 6
* HO2	HD OUT output on pin 2
* HO4	HD OUT output on pin 4
* HO33	HD OUT output on pin 33
* EO6	ED OUT output on pin 6
* EO29	ED OUT output on pin 29
HOIV	HD OUT level invert, LOW: HD
EOIV	ED OUT level invert, LOW: ED
DIV	HD IN/ED IN level assignment
MSC	



Strap post layout

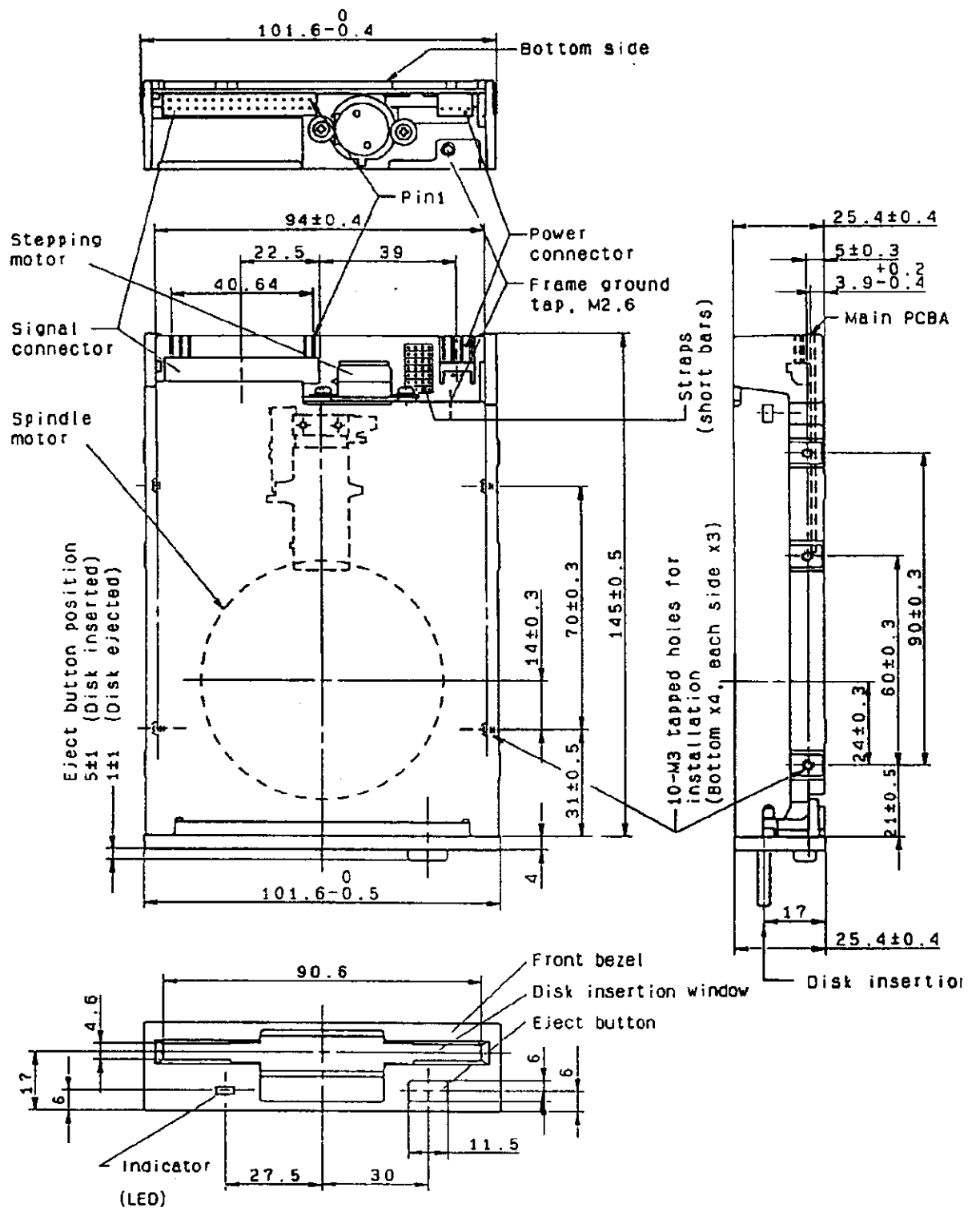
Note: * straps overlap with other strap posts and functions. Select appropriate straps unless overlapping.

OUTLINE

This specification provides a description for the TEAC FD-235J, 4M/2M/1M bytes, extra density micro-floppy disk drive (hereinafter referred to as the FDD). Table 1 shows the outline of the FDD.

Model name	FD-235J-5XXX		
Safety standard on label	UL & CSA		
Operation modes	1MB mode Write/Read	2MB mode Write/Read	4MB mode Write/Read
Disk used	Normal density 2DD	High density 2HD	Extra density 2ED
Data transfer rate	250k bits/sec	500k bits/sec	1M bits/sec
Disk rotational speed	300rpm		
Track density	135tpi		
Track to track time	3msec		
Required power	+5V single (4.5-5.5V)		

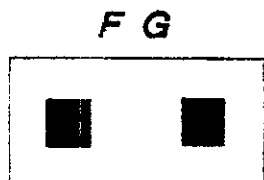
Signal interface	TTL, refer to item 8.1.
Input signal terminator	1k Ω \pm 5%, unremovable
Customer selectable straps	25 selections
Strap setting at delivery	<ol style="list-style-type: none"> 1. DS1 : DRIVE SELECT 1 on pin 12 2. HI2 : HD IN on pin 2 (Low level is HD) 3. EI33: ED IN on pin 33 (High level is ED) 4. HO27: HD OUT on pin 27 (Low level is HD) 5. EO17: ED OUT on pin 17 (Low level is ED) 6. LHI : High density at Low of HD IN 7. OIV : High/Extra density at Low of HD/ED IN 8. G29 : DC 0V on pin 29 9. FG : Frame is electrically shorted to DC 0V.
PCBA function setting	<ol style="list-style-type: none"> 1. Auto-recalibration, Auto-chucking 2. LED turns on at DRIVE SELECTed. 3. Ready and seek-complete gate (Full-mask) for INDEX and READ DATA output pulses. 4. DISK CHANGE on pin 34 5. With power connector
Other optional function & mechanism	Not equipped.



(Units:mm)

External view

FD235J-5631 or J-5637 strap (jumper) settings



Recommended Strap settings for floppy disk controllers that support the 2.88 Mb format.

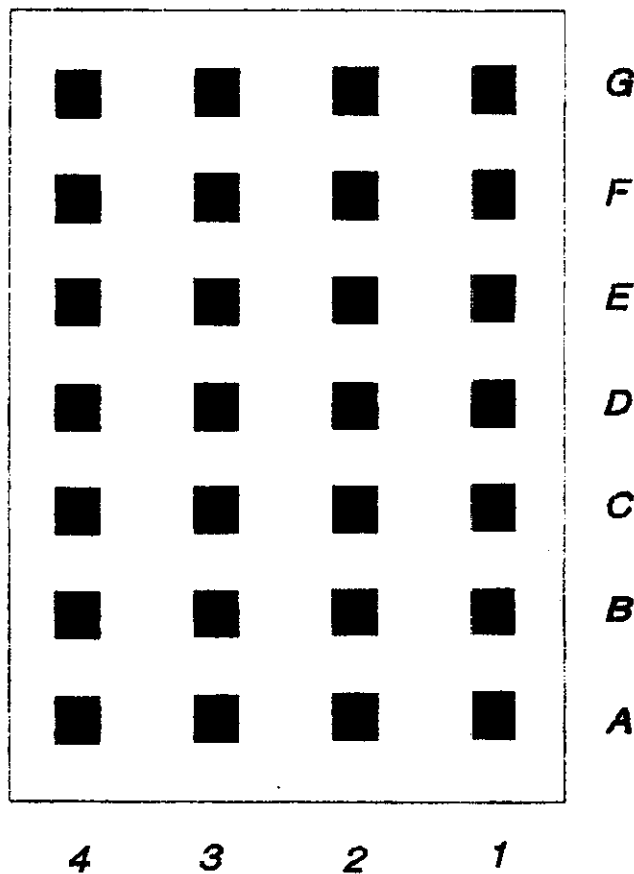
Factory Defaults are:

A1-B1, C3-D3, D4-E4,

F3-F4, E2-F2, B4-C4,

G2-G3, E1-F1 and FG.

for other straps definition and locations please refer to the following page



Most 2.88MB controllers

*A1-B1
B2-C2
D3-E3*

FG

GSI 2.88MB controller

*drive select 1
extra density sensor
high density sensor
extra density out
high density out
frame ground*

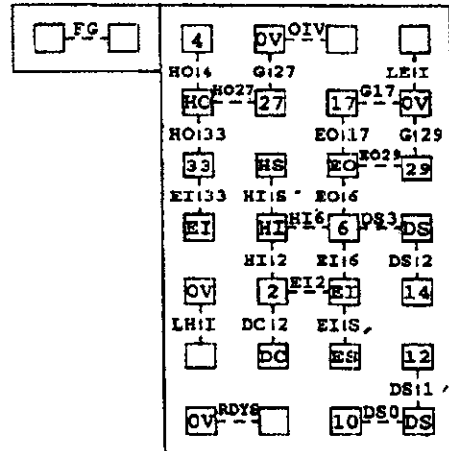
*A1-B1
B2-C2
D3-E3
E1-E2
E4-F4
FG*

11. CUSTOMER SELECTABLE STRAPS

11.1 Function Summary of Straps

The FDD is equipped with the following selectable straps on the main PCBA. Insertion of a short bar onto the post pin is defined as the on-state of the strap. Refer to Table 1 in item 1 as to the strap setting at delivery.

Strap	Function
DS0	DRIVE SELECT 0 on pin 10
DS1	DRIVE SELECT 1 on pin 12
DS2	DRIVE SELECT 2 on pin 14
DS3	DRIVE SELECT 3 on pin 6
RDYS	Select DISK CHANGE or READY output on pin 34 (on: READY)
DC2	DISK CHANGE output on pin 2
HIS	Automatic HD set by HD sensor
HI2	HD IN input on pin 2
HI6	HD IN input on pin 6
EIS	Automatic ED set by ED sensor
EI2	ED IN input on pin 2
EI6	ED IN input on pin 6
EI33	ED IN input on pin 33
HO4	HD OUT output on pin 4
HO27	HD OUT output on pin 27
HO33	HD OUT output on pin 33
EO6	ED OUT output on pin 6
EO17	ED OUT output on pin 17
EO29	ED OUT output on pin 29
OIV	HD OUT/ED OUT level invert, LOW: TRUE
LHI	HD IN level invert, LOW: 2MB mode
LEI	ED IN level invert, LOW: 4MB mode
G17	DC 0V on pin 17
G27	DC 0V on pin 27
G29	DC 0V on pin 29
FG	Short between FDD Frame and DC 0V



Strap post layout

Note: * straps overlap with other strap posts and functions. Select appropriate straps unless overlapping.

(Table 9) Function summary of straps